

## ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

<b>Title of Invention</b>	<b>A METHOD, SYSTEM, AND COMPUTER PROGRAM PRODUCT FOR GENERATING AND VERIFYING ISOLATION LOGIC MODULES IN DESIGN OF INTEGRATED CIRCUITS</b>																																																				
 Application Number : 10/711,493 Confirmation Number: First Named Applicant: Bhanu KAPOOR Attorney Docket Number: Q82851 Art Unit: 2128 Examiner: R. Frejd Search string: ( 5483176 or 6083271 or 6600358 or 6711719 or 6717452 ).pn  <b>US Patent Documents</b>  <b>Note: Applicant is not required to submit a paper copy of cited US Patent Documents</b> <table border="1"><thead><tr><th>init</th><th>Cite.No.</th><th>Patent No.</th><th>Date</th><th>Patentee</th><th>Kind</th><th>Class</th><th>Subclass</th></tr></thead><tbody><tr><td>/RF/</td><td>1</td><td>5483176</td><td>1996-01-09</td><td>Rodriquez</td><td></td><td></td><td></td></tr><tr><td>↓</td><td>2</td><td>6083271</td><td>2000-07-04</td><td>Morgan</td><td></td><td></td><td></td></tr><tr><td>↓</td><td>3</td><td>6600358</td><td>2003-07-29</td><td>Chan</td><td></td><td></td><td></td></tr><tr><td>↓</td><td>4</td><td>6711719</td><td>2004-03-23</td><td>Cohn</td><td></td><td></td><td></td></tr><tr><td>/RF/</td><td>5</td><td>6717452</td><td>2004-04-06</td><td>Carpenter</td><td></td><td></td><td></td></tr></tbody></table> <b>Signature</b> <table border="1"><thead><tr><th>Examiner Name</th><th>Date</th></tr></thead><tbody><tr><td>/Russell Frejd/</td><td>06/25/2007</td></tr></tbody></table>		init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass	/RF/	1	5483176	1996-01-09	Rodriquez				↓	2	6083271	2000-07-04	Morgan				↓	3	6600358	2003-07-29	Chan				↓	4	6711719	2004-03-23	Cohn				/RF/	5	6717452	2004-04-06	Carpenter				Examiner Name	Date	/Russell Frejd/	06/25/2007
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